Interference Search Notes

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(FPGA PLD PLA) with (overwrite reprogram) with (boundar$4 word frame)	US-PGPUB	OR	ON	2006/07/12 12:31
L2	118	(FPGA PLD PLA firmware) with (overwrit$4 reprogram$8 reconfig$8 flash) with (boundar$4 word frame)	US-PGPUB	OR	ON	2006/07/12 12:34
L3	2	(FPGA PLD PLA firmware) with (overwrit$4 reprogram$8 reconfig$8 flash) with (boundar$4 word frame) with (align set determine detect)	US-PGPUB	OR	ON	2006/07/12 12:33
L4	472	(FPGA PLD PLA firmware) with (overwrit$4 reprogram$8 reconfig$8 flash) with (partial portion section part)	US-PGPUB	OR	ON	2006/07/12 12:34
L5	161	(FPGA PLD PLA firmware) with ((overwrit$4 reprogram$8 reconfig$8 flash) near3 (partial portion section part))	US-PGPUB	OR	ON	2006/07/12 12:35
L6	3	(FPGA PLD PLA firmware) with ((overwrit$4 reprogram$8 reconfig$8 flash) near3 (partial portion section part)) with (word frame boundar$4)	US-PGPUB	OR	ON	2006/07/12 12:36
L7	3	(FPGA PLD PLA firmware) with ((overwrit$4 reprogram$8 reconfig$8 flash) near3 (partial portion section part)) with (word frame boundar$4 align)	US-PGPUB	OR	ON	2006/07/12 12:36

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S25	365	((FPGA PLA PLD) and (FSM (finite adj state adj machince)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 16:29
S46	98	S44 and S45	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 16:30
S45	20187	align with (word frame boundary)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 16:30
S47	204012	FPGA PLA PLD (program$5 adj (device circuit element))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 17:53
S44	528	((FPGA PLA PLD (program$5 adj (device circuit element))) and (FSM (finite adj state adj machince)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 17:53
S50	2	S49 with S45	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 17:55
S49	13159	S48 with (mode function vary$4 vari$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 17:55
S52	5	S49 same S45	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 17:56

7/12/2006 12:00:19 PM
C:\Documents and Settings\ahassan\My Documents\EAST\Workspaces\search on 11_29_05 for app 10_699012.wsp

Page 1

EAST Search History

S51	91	S49 and S45	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 17:57
S48	204012	FPGA PLA PLD (program$5 adj (device circuit element)) (reconfig$7 adj program$5 adj (device circuit element))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 17:59
S55	149	S53 with (word frame boundar$4) with (align set adjust)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 18:00
S54	1784	S53 with (word frame boundar$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 18:00
S56	1	S53 with (word frame boundar$4) with (align set adjust) with ((finite adj state adj machine) fsm)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/06 18:01
S53	54949	FPGA (program$5 adj (device circuit element)) (reconfig$7 adj program$5 adj (device circuit element))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/10 10:35
S59	42473	S57 and (mode function)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 10:37
S57	55013	FPGA (program$5 adj (device circuit element)) (reconfig$7 adj program$5 adj (device circuit element))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/10 10:37

S62	6088	S60 with (mode function)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/10 10:38
S61	49015	S60 and (mode function)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/10 10:38
S58	39366	S57 and (mode function)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/10 10:38
S63	3566	S60 near5 (mode function)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/10 10:40
S65	404533	(word fram$3 boundar$4) with (align set adjust detect find)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 11:58
S64	25	S62 with (word frame boundar$4) with (align set adjust detect find)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 11:59
S60	67198	FPGA (program$5 adj (device circuit element)) (reconfig$7 adj program$5 adj (device circuit element))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 11:59
S66	67864	FPGA (program$5 adj (device circuit element)) (reconfig$7 adj program$5 adj (device circuit element)) (reprogram$7 adj (device circuit element))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 12:02

EAST Search History

S68	84784	(FPGA (field adj programmable adj gate adj array)) ((pla pld) and program$7) (program$5 adj (device circuit element)) (reconfig$7 adj program$5 adj (device circuit element)) (reprogram$7 adj (device circuit element))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 12:04
S67	263	S66 with (word frame boundar$4) with (align set adjust detect find)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 12:04
S69	15	S66 with (word frame) with (limit boundar$4) with (align set adjust detect find determin$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 12:06
S70	16	S66 with (word frame packet datagram double?word byte) with (limit boundar$4) with (align set adjust detect find determin$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 12:07
S71	1666	(overwrit$3 reprogram$8 reconfig$8 flash) with (boundar$4 (field near2 (length size)) border)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 16:33
S74	0	S72 with (fsm (finite adj state adj machine))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 16:34
S73	0	S71 with (fsm (finite adj state adj machine))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 16:34
S75	61	S71 and (fsm (finite adj state adj machine))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 17:11
S76	19	S71 with ((circuit component) near5 (first second multi$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 17:20

S78	4	S77 and (sdh sonet)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 17:31
S80	2	S77 and S79	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 17:34
S79	2843	(detect$4 determin$4) with (frame boundar$4) with (align$4 adjust$5) with (data input stream word packet)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 17:35
S82	6	S77 and S81	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 17:36
S81	13436	(detect$4 determin$4) with (frame boundar$4) with (align$4 adjust$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 17:36
S84	39	S71 with (pld pla fpga firmware)	US-PGPUB; USPAT	OR	ON	2006/07/10 17:38
S83	257	S71 with (partial portion section part)	US-PGPUB; USPAT	OR	ON	2006/07/10 17:38
S85	5	S77 with (pld pla fpga firmware)	US-PGPUB; USPAT	OR	ON	2006/07/10 17:39
S87	2	S77 and S86	US-PGPUB; USPAT	OR	ON	2006/07/10 17:43

S86	27	(US-20050253617-$ or US-20040249757-$ or US-20050166040-$ or US-20010002199-$ or US-20050177633-$ or US-20040243978-$ or US-20040223010-$ or US-20040003355-$ or US-20030161355-$ or US-20030193962-$ or US-20050218984-$ or US-20040264496-$ or US-20030187629-$ or US-20030221013-$ or US-20030206559-$ or US-20020163932-$ or US-20040240598-$ or US-20040258058-$ or US-20020009048-$).did. or (US-6940309-$ or US-5412260-$ or US-6795451-$ or US-5033044-$ or US-6751238-$ or US-6898204-$ or US-6970966-$ or US-5867507-$). did.	US-PGPUB; USPAT	OR	ON	2006/07/10 17:43
S88	1	S71 with S79	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 17:48
S89	107	S72 with (partial portion section part)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 17:49
S77	310	S71 with (partial portion section part)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 17:49
S72	798	(overwrit$3 reprogram$8 reconfig$8 flash) near5 (boundar$4 (field near2 (length size)) border)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 17:49
S90	89	S72 with (partial portion section part)	US-PGPUB; USPAT	OR	ON	2006/07/11 16:10